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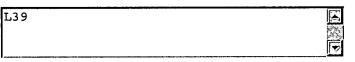
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<u>L39</u>	L34 and 117	1	<u>L39</u>
<u>L38</u>	L34 and 18	33	<u>L38</u>
<u>L37</u>	L34 and 17	25	<u>L37</u>
<u>L36</u>	L34 and 15	459	<u>L36</u>
<u>L35</u>	L34 and l4	96	<u>L35</u>
<u>L34</u>	130 and register\$1 near12 (stack or memor\$4 or stor\$5 or ram or rom or eprom or eeprom or sram or dram)	459	<u>L34</u>
<u>L33</u>	130 and (stack or memor\$4 or stor\$5 or ram or rom or eprom or eeprom or sram or dram)	722	<u>L33</u>
<u>L32</u>	L28 and 18	465	<u>L32</u>
<u>L31</u>	L28 and 17	29	<u>L31</u>
<u>L30</u>	L28 and 15	722	<u>L30</u>

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<u>L29</u>	L28 and 14	2055	<u>L29</u>
<u>L28</u>	(single or one) near2 (instruction or command or macro near1 instruction or macroinstruction or microinstruction) near12 (plur\$7 or multipl\$7 or two or dual or second) near8 (transfer\$5 or read\$3 or writ\$6 or operation\$1 or move\$1)	7908	<u>L28</u>
<u>L27</u>	(single or one) near5 (instruction or command or macro near1 instruction or macroinstruction or microinstruction) near12 (plur\$7 or multipl\$7 or two or dual or second) near8 (transfer\$5 or read\$3 or writ\$6 or operation\$1 or move\$1)	13899	<u>L27</u>
<u>L26</u>	L25 and 14	.3211	<u>L26</u>
<u>L25</u>	(single or one) near5 (instruction or command or macro near1 instruction or macroinstruction or microinstruction) near18 (plur\$7 or multipl\$7 or two or dual or second) near10 (transfer\$5 or read\$3 or writ\$6 or operation\$1 or move\$1)	15831	<u>L25</u>
<u>L24</u>	13 and 117	0	<u>L24</u>
<u>L23</u>	13 and 18	6	<u>L23</u>
<u>L22</u>	13 and 17	1	<u>L22</u>
L21	13 and 15	13	<u>L21</u>
L20	13 and 14	17	<u>L20</u>
DB=	=USPT; PLUR=YES; OP=OR		
<u>L19</u>	12 and 117	0	<u>L19</u>
DB=	=PGPB, USPT; PLUR=YES; OP=OR		
<u>L18</u>	(717/136-143)![CCLS]	2514	<u>L18</u>
<u>L17</u>	(717/136-143)[CCLS]	2514	<u>L17</u>
L16	11 and 18	32	<u>L16</u>
L15	11 and 17	3	<u>L15</u>
<u>L14</u>	11 and 15	30	<u>L14</u>
<u>L13</u>	11 and 14	104	<u>L13</u>
<u>L12</u>	12 and 18	15	<u>L12</u>
<u>L11</u>	12 and 17	2	<u>L11</u>
<u>L10</u>	12 and 15	14	<u>L10</u>
<u>L9</u>	12 and 14	34	<u>L9</u>
<u>L8</u>	(712/200-203,225-228)![CCLS]	3118	<u>L8</u>
<u>L7</u>	(711/131,132)![CCLS]	372	<u>L7</u>
<u>L6</u>	711/131,132	0	<u>L6</u>
L5	(711/131-172,220-221)![CCLS]	21360	<u>L5</u>
<u>L4</u>	(712/2-300)[CCLS]	13526	<u>L4</u>
DB=	=PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=OR		
<u>L3</u>	L1 near45 (transfer\$5 or sent or send\$3 or mov\$3 or writ\$5 or load\$3 or stor\$4) near10 (register\$1) near35 (stack or memor\$4 or stor\$5 or ram or rom	43	<u>L3</u>
	or eprom or eeprom or sram or dram)		
<u>L2</u>	L1 near45 (transfer\$5 or sent or send\$3 or mov\$3 or writ\$5 or load\$3 or stor\$4) near10 (register\$1)	70	<u>L2</u>
<u>L1</u>	(macro\$1 or macro near1 instruction\$1) near15 (push\$5 or pop\$5 or mov\$3)	2716	<u>L1</u>



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